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Editorial Board

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Ms.J.Jenisha, AP/ECE

Students

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S.P.Devasri, III ECE

The Department of Electronics and Communication Engineering was established in the year 2007. The department has an intake of 60 students in B.E. course. The department possesses the most advanced equipment in its laboratories. It also provides opportunities to grow and excel in the technical world by conducting regular workshops and programs in various fields. The department is highly active in research work in the fields of broadband communications, VLSI Design, image processing etc. The students are provided adequate training in the field of signal processing, image processing and digital communication. Students are highly encouraged to undergo in-plant training at some of the most prestigious organizations during their time at the institution.

Vision of the Department:

1. To provide the quality education in the field of Electronics and Communication Engineering which caters the needs of the society in line with the technological revolution.

Mission of the Department:

1. To upgrade the technical knowledge of the students continuously by providing industrial exposure and innovative projects.
2. To establish a creative learning environment for the students by active learning of the techniques in the electronics and communication engineering field.
3. To nurture career improvement by facilitating skill development and training in the recent technologies.

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

PEO1: To provide the students with a strong foundation in the required sciences in order to pursue studies in Electronics and Communication Engineering.

PEO2: To gain adequate knowledge to become good professional in electronic and communication engineering associated industries, higher education and research.

PEO3: To develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.

PEO4: To prepare students to critically analyze existing literature in an area of specialization and ethically develop innovative and research oriented methodologies to solve the problems identified.

PEO5: To inculcate in the students a professional and ethical attitude and an ability to visualize the engineering issues in a broader social context.

PROGRAM SPECIFIC OUTCOMES (PSOs)

PSO1: Design, develop and analyze electronic systems through application of relevant electronics, mathematics and engineering principles

PSO2: Design, develop and analyze communication systems through application of fundamentals from communication principles, signal processing, and RF System Design & Electromagnetism.

PSO3: Adapt to emerging electronics and communication technologies and develop innovative solutions for existing and newer problems.

ECE ASSOCIATION ACTIVITIES

IEI STUDENT CHAPTER INAUGURATION

The Student Chapter of the Institution of Engineers (India) for the departments of AI & DS, CSE, ECE, and EEE at Chettinad College of Engineering and Technology, Puliur, Karur, was successfully inaugurated on Friday, 31st January 2025, in the presence of student members, faculty, and staff. The inauguration was graced by Dr. S. Dharmalingam, Former General Manager, BHEL, Trichy; Council Member, and Chairman, CCC, IEI, who formally inaugurated the Student Chapter. In his inaugural address, he encouraged students to actively participate in the various activities planned under the chapter. The event was Presided over by Dr. (Mrs.) A. Punitha, Principal, and was organized by Mr. B. Sathishkumar, Head-Admin, and was coordinated by Dr. M. Kumar, HoD/ECE. A total of 231 students, faculty members, and non-teaching staff attended the function, contributing to its grand success.

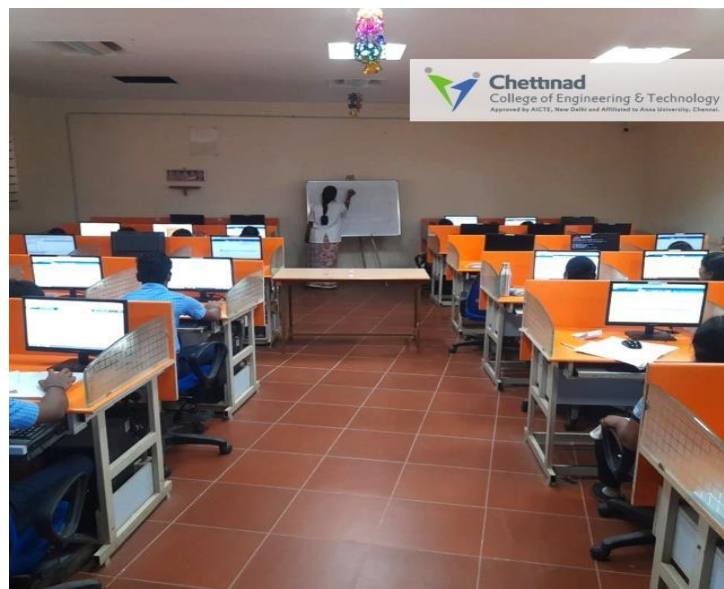
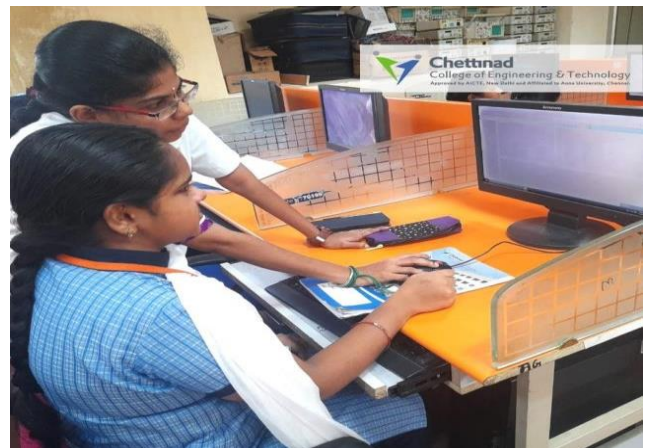


VALUE ADDED COURSES

SYSTEM VERILOG FOR FUNCTIONAL VERIFICATION: CONCEPTS TO PRACTICE

Our Department of Electronics and Communication Engineering successfully conducted a Four Day Value Added Course on “System Verilog for Functional Verification: Concepts to Practice” for our III-year ECE Students handled by resource person Mr.V.Govindaraj, Intel Technology India Private Limited, Bangalore and Internal Faculty Members Ms.D.Ragavi, Assistant Professor, ECE and Mrs.P.Nagarani Sobana, Assistant Professor, ECE from 30.01.2025 to 02.02.2025. The course aimed to provide students with fundamental knowledge

of digital design and verification techniques and covered Verilog basics, System Verilog concepts, and hands-on sessions on functional verification.

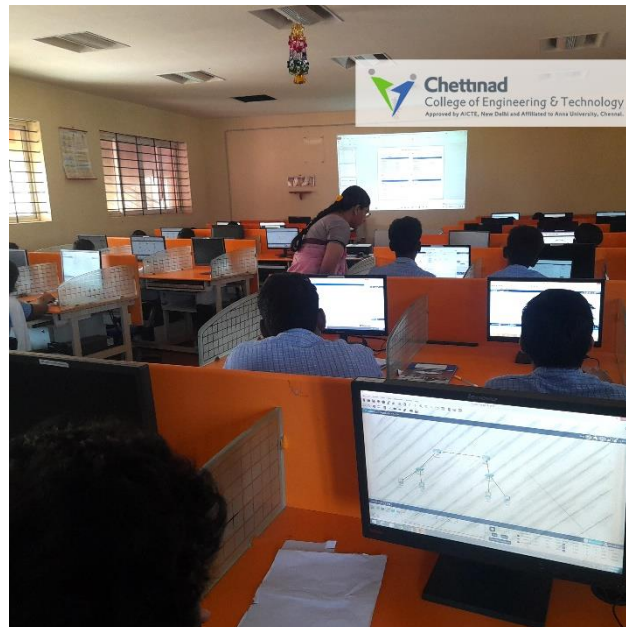
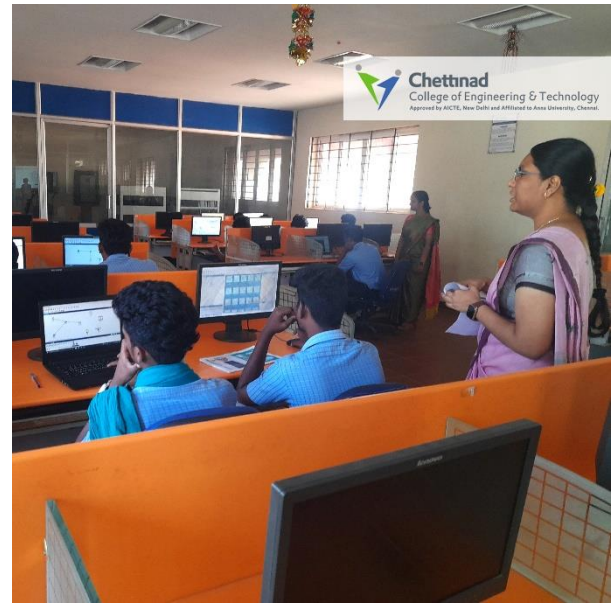
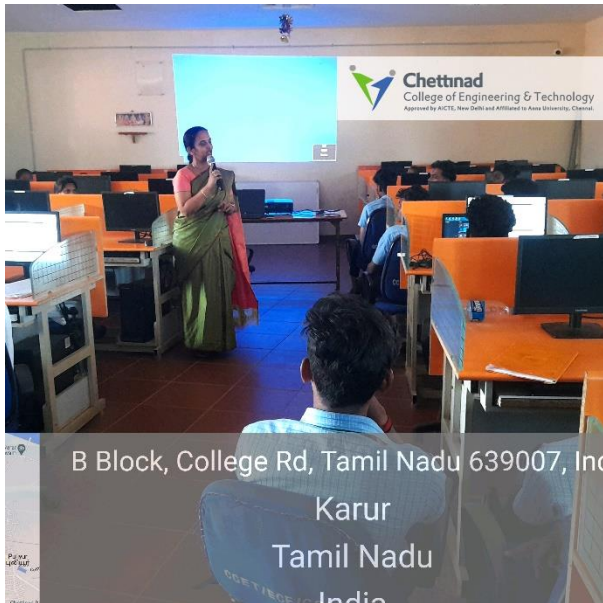


IOT SYSTEMS USING CISCO PACKET TRACER

The Department of Electronics and Communication Engineering and IEI Student Chapter successfully organized a three-day Value Added Course on “IoT Systems using CISCO Packet Tracer” for second-year ECE students from January 24, 2025 to January 28, 2025. The sessions were conducted by resource persons Ms. G. Rajeswari (AP/ECE), Ms. R. S. Mathubala (AP/ECE) from Vivekanandha College of Engineering for Women (Autonomous), along with internal faculty Mrs. A. Karthikeyani (AP/ECE).

The course provided a strong foundation in networking concepts, including network devices, media, protocols, and IoT systems. Students gained hands-on experience with the CISCO Packet Tracer tool, observing data flow within a network and performing basic device configurations to establish network connections using switches and routers. Additionally, they implemented a webpage using DNS and a Web Server.

By the end of the course, students were able to demonstrate how IP addresses facilitate network communication, create simple Local Area Networks (LANs), and simulate IoT-based applications using Cisco Packet Tracer.



STUDENT ACHIEVEMENTS

NPTEL ACHIEVERS

Our ECE III-year and IV-year students achieved great success in NPTEL.

Counselling Code
2630



Chettinad
College of Engineering & Technology

Congratulations!

to our III ECE Students
for their outstanding achievement
in NPTEL Examination.

Title:
Digital VLSI Testing

July - October 2024



SRINITHI. R



YUVASHREE. N

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College of Engineering & Technology
Approved by AICTE-New Delhi and Affiliated to Anna University-Chennai.

NPTEL
swayam

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NPTEL
swayam

Congratulations!

IV YEAR ECE - OUTSTANDING NPTEL ACHIEVEMENTS



JEEVITHA M



DIVYA



HARISH R



GOWSALYA G



Skill India
कौशल भारत - कुशल भारत

Title : **Programming in JAVA**
(July - October 2024)

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Skill India
कौशल भारत - कुशल भारत


NPTEL
swayam

Congratulations!


IV YEAR ECE - OUTSTANDING NPTEL ACHIEVEMENTS




ABISHEK S



Title : **Programming in Modern C++**
(Aug - October 2024)



BALAMURUGAN K



Title : **Google Cloud Computing Foundations**

www.chettinadtech.ac.in

PRIZE WINNERS IN INTER COLLEGE EVENTS

Jothika G., Lakshana S., Jeya Parabha R., and Jeevitha T. of II Year ECE secured the Third Prize in the Paper Presentation event at the Technical Symposium Fractals'25 held on 27.02.2025 at Sona College of Technology.



PLACEMENTS

Sozhaeswaran P. and Divya K. have been selected as Design Trainees – VLSI Design at Tessolve Semiconductor Pvt. Ltd





Chettinad

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